Unified Parallel C

An overview

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Agenda

Productivity, Performance and Parallelism
 Execution models

 Message Passing
 Shared Memory
 Partitioned Global Address Space

 Overview of UPC
 Discussion

Languages Related to C

co-array added to the FORTRAN standard



Productivity -- Ease of Getting Performance

Single thread performance
Clock frequencies leveling off, power limitations
Memory is getting farther away
HW architecture response
Multi-core
More levels in the memory hierarchy
Accelerators
Speculation

Complexity in HW can hurt programmer productivity

Execution Models

Message Passing



Each processor has local address space. Interact via explicit communication. (MPI)

Shared Memory



Multiple threads running concurrently. One address space. (OMP)

Partitioned Global Address Space



Multiple threads

Memory accessible by all ...

Partitioned Global Address Space



Execution Models

Process/Thread

Address Space

Shared Memory pThreads, OpenMP, Java

Accelerator Address Space • Accelerator Thread

UDA, OpenCL



- Computation is performed in
- Computation is performed multiple places.
- A place contains data that can be operated on remotely.
- Data lives in the place it was created, for its lifetime.

- A datum in one place may reference a datum in another place.
- Data-structures (e.g. arrays) may be distributed across many places.
- Places may have different computational properties

Extension to ISO C

A PGAS language

C's design philosophy

- Programmer is knowledgeable
- Minimal language facility to support the right level abstraction, but not too much to hide underlying hardware
 - Close to the hardware when needed
 - Performance without extensive programming effort
 - Code easy to understand and maintenance
- Specification V1.0 completed Feb 2001
- Current specification V1.2
 - http://upc.gwu.edu/
 - http://upc.lbl.gov/publications/

What is UPC ...

intrepid

www.intrepid.com/upc

HP /ww.hp.com/go/upc **TotalView**

www.etnus.com

Michigan Tech

www.upc.mtu.edu

CRAY

www.cray.com

Berkeley

upc.lbl.gov

IBM

GCC

www.alphaworks.ibm.com/tech/upccompiler

Quick Overview of the UPC Language



hello word:

```
shared int x;
int y;
int main() {
    printf("hello %d\n", MYTHREAD);
```

hello word:

One copy shared by all threads

shared int x; int y; int main() { printf("hello %d\n", MYTHREAD);

hello word:

Private - one per thread

shared int x;
int y;
int main() {
 printf("hello %d\n", MYTHREAD);

hello word:

A compiler predefined variable

shared int x; int y; int main() { printf("hello %d\n", MYTHREAD);



hello word:

```
shared int x;
int y;
int main() {
    printf("hello %d\n", MYTHREAD);
}
```

hello 0 hello 1 hello 2 Same code executed by all threads -SPMD

hello word:



private

UPC



shared int x; int y; int main() { printf("hello %d\n", MYTHREAD); }

hello 0 hello 1 hello 2 hello 3 Same code executed by all threads -SPMD



shared int arr[THREADS * 3];

Compiler generated var – no. of threads

shared array:



All threads can access all elements

Elements are distributed – with affinity to threads.





shared int arr[THREADS * 3];
...
upc_forall(i=0; i<THREADS*3; ++i; i)
arr[i] = 0;</pre>







shared int arr[THREADS * 3];
...
upc_forall(i=0; i<THREADS*3; ++i; i) {
 if (i%THREADS == MYTHREAD)
 arr[i] = 0;
 Transformed
</pre>

Transform to a for loop



shared int arr[THREADS * 3];
...
upc_forall(i=0; i<THREADS*3; ++i; &arr[i])
arr[i] = 0;</pre>









Transform to a for loop



shared[3] int arr[THREADS * 3];



elements are distributed by blocks of 3

Shared pointers shared int *p; shared int * shared p; Memory management upc_global_alloc, upc_local_alloc, upc_free Synchronization upc_lock, upc_unlock upc_barrier, upc_fence, upc_wait, upc_notify Utility functions upc_memcpy, upc_memput, upc_memget, upc_memset Memory consistency model Strict/relaxed

PGAS languages

Features

Small set of data parallel primitives

typically

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rafted on an existing language: Co-Array Fortran, UPC, Titanium

 Shared memory-like programming with locality awareness – shared data is

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xplicitly declared and distributions are implicit in the declarationSPMD threading

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odel with synchronization primitives (barriers, fences, and locks)

- Collective communication and parallel I/O through libraries
- Implementation
 - Can be mapped to shared

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emory, distributed memory and combinations (clusters of SMPs)

Discussions

Discussion

Given the trend in hardware architecture, should C add features to support parallel programming ?

- From C's perspective, is something like UPC attacking the problem at the right level and scope ?
- What should be the role of the language standard ?
- How could the C committee be involved ...
 - Adviser to the UPC Working groups ?
 - Study group within the C committee ?
 - Technical report ?

